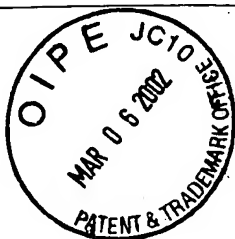




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March 6, 2002

ATTORNEY DOCKET NO. 6502.0372  
SUN DOCKET NO. P5260  
CUSTOMER NO. 22,852

Assistant Commissioner for Patents  
Washington, DC 20231

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JUL 22 2002

U.S. Patent Application Serial No. 10/035,741

Title: FLOATING POINT STATUS INFORMATION TESTING CIRCUIT

Inventor: Guy L. Steele, Jr.

Technology Center 2100

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Information Disclosure Statement - 2 pgs.
2. Form 1449 - 1 pg
3. Post Card

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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

By: F. Leslie Bessenger III  
F. Leslie Bessenger, III  
Reg. No. 39,108

FLB/tlc  
Enclosures



#4  
CE  
7/31/02

PATENT  
Customer No. 22,852  
Sun Docket No. P5260  
Attorney Docket No. 6502.0372

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
)  
Guy L. STEELE, JR. )  
)  
Application No.: 10/035,741 )  
)  
Filed: December 28, 2001 )  
)  
For: FLOATING POINT STATUS )  
INFORMATION TESTING )  
CIRCUIT )

Group Art Unit: Not Assigned

Examiner: Not Assigned

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Commissioner for Patents and Trademarks  
Washington, DC 20231

Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicant brings to the attention of the Examiner the documents listed on the attached PTO 1449. This Information Disclosure Statement is being filed within three months of the filing date of the above-referenced application.

Copies of the listed documents and any copending applications were previously submitted in a related application, application no. 10/035,747, filing date December 28, 2001.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the

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documents as prior art against any claim in the application and Applicant determines that the cited documents do not constitute "prior art" under United States law, Applicant reserves the right to present to the office the relevant facts and law regarding the appropriate status of such documents.

Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

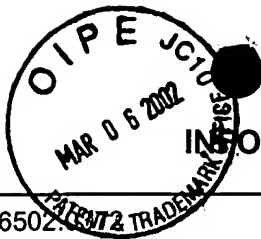
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## INFORMATION DISCLOSURE CITATION

Atty. Docket No.	6502	Serial No.	10/035,741
Applicant	Guy L. Steele, Jr.		
Filing Date	December 28, 2001	Group:	Unknown

## U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
	5,347,481	9/13/94	Williams	364	748	2/1/93
	5,347,482	9/13/94	Williams	364	757	12/14/92
	6,131,106	10/10/00	Steele, Jr.	708	510	1/30/98

## FOREIGN PATENT DOCUMENTS

Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
					RECEIVED
					JUL 22 2002
					Technology Center 2100

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Title: "Safe Treatment of Overflow and Underflow Conditions", by Robert A. Fraley & J. Stephen Walther, Hewlett-Packard Co., pgs. 1-5.
	Title: "Vax Floating Point: A Solid Foundation for Numerical Computation", by Mary Payne & Dileep Bhandarkar, Digital Equipment Corp., pgs. 1-12.
	Title: Lecture Notes on the Status of "IEEE Standard 754 for Binary Floating-Point Arithmetic", by Prof. W. Kahan, May 31, 1996, pgs. 1-30.
	Title: "Interval Arithmetic Specification" by Dmitri Chiriaev & G. William Walster, Draft revised May 4, 1998, pgs. 1-78.
	Title: "IEEE Standard for Binary Floating-Point Arithmetic IEEE Standard 754-1985," by Standards Committee of the IEEE Computer Society, The Institute of Electrical and Electronics Engineers, Inc., copyright 1985, pgs. 1-14.

Examiner	Date Considered
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce